



Docket No.: H6808.0004/P004
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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626-02
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In re Patent Application of:
Atsushi Takane et al.

Application No.: 10/082,286

Group Art Unit: 2878

Filed: February 26, 2002

Examiner: Not Yet Assigned

For: SEMICONDUCTOR INSPECTION
SYSTEM

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents
Washington, DC 20231

Dear Sir:

Pursuant to 37 CFR § 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference listed on the attached Form PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom. A copy of the listed reference is attached, together with an English-language translation of the Abstract.

This Information Disclosure Statement is being filed within three months of the U.S. filing date and before the mailing of a first Office Action on the merits as far as is known to the undersigned.

This Information Disclosure Statement is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" unless specifically designated as such. In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made. It is submitted that the Information Disclosure Statement is in compliance

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with 37 CFR § 1.98 and the Examiner is respectfully requested to consider the listed reference.

The Commissioner is hereby authorized to charge any deficiency in the fees filed, asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm), to Deposit Account No. 04-1073, under Order No. H6808.0004/P004.

Dated: May 28, 2002

Respectfully submitted,

By 

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